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 Pages:262 - 263

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2 A high-speed processor for digital sine/cosine generation and angle rotation

Dengwei Fu; Willson, A.N., Jr.;
 Signals, Systems & Computers, 1998. Conference Record of the Thirty-Second Asilomar Conference on , Volume: 1 , 1-4 Nov. 1998
 Pages:177 - 181 vol.1

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3 A memory-efficient and high-speed sine/cosine generator based on parallel CORDIC rotations

Shen-Fu Hsiao; Yu-Hen Hu; Tso-Bing Juang;
 Signal Processing Letters, IEEE , Volume: 11 , Issue: 2 , Feb. 2004
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4 Quadrature direct digital frequency synthesizers using interpolation-based angle rotation

Yongchul Song; Beomsup Kim;
 Very Large Scale Integration (VLSI) Systems, IEEE Transactions on , Volume: 12 , Issue: 7 , July 2004
 Pages:701 - 710

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5 Fast and predictive HVDC extinction angle control

Tamai, S.; Naitoh, H.; Ishiguro, F.; Sato, M.; Yamaji, K.; Honjo, N.;
 Power Systems, IEEE Transactions on , Volume: 12 , Issue: 3 , Aug. 1997
 Pages:1268 - 1275

[\[Abstract\]](#) [\[PDF Full-Text \(948 KB\)\]](#) **IEEE JNL**

6 Quadrature direct digital frequency synthesizer using an angle rotation algorithm

Curticapean, F.; Palomaki, K.I.; Niittylahti, J.;
Circuits and Systems, 2003. ISCAS '03. Proceedings of the 2003 International Symposium on , Volume: 2 , 25-28 May 2003
Pages:II-81 - II-84 vol.2

[\[Abstract\]](#) [\[PDF Full-Text \(336 KB\)\]](#) [IEEE CNF](#)

7 A 16 b quadrature direct digital frequency synthesizer using interpolative angle rotation algorithm

Yongchul Song; Beomsup Kim;
VLSI Circuits Digest of Technical Papers, 2002. Symposium on , 13-15 June 2002
Pages:146 - 147

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8 A 100-MHz, 16-b, direct digital frequency synthesizer with a 100-dBc spurious-free dynamic range

Madisetti, A.; Kwentus, A.Y.; Willson, A.N., Jr.;
Solid-State Circuits, IEEE Journal of , Volume: 34 , Issue: 8 , Aug. 1999
Pages:1034 - 1043

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9 Redundant CORDIC methods with a constant scale factor for sine and cosine computation

Takagi, N.; Asada, T.; Yajima, S.;
Computers, IEEE Transactions on , Volume: 40 , Issue: 9 , Sept. 1991
Pages:989 - 995

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10 The fast adaptive ROTOR's RLS algorithm

Cioffi, J.M.;
Acoustics, Speech, and Signal Processing [see also IEEE Transactions on Signal Processing], IEEE Transactions on , Volume: 38 , Issue: 4 , April 1990
Pages:631 - 653

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11 A new method to implement CORDIC algorithm

Kharrat, M.W.; Loulou, M.; Masmoudi, N.; Kamoun, L.;
Electronics, Circuits and Systems, 2001. ICECS 2001. The 8th IEEE International Conference on , Volume: 2 , 2-5 Sept. 2001
Pages:715 - 718 vol.2

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